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1 Strategic directions in constraint programming

Pascal Van Hentenryck, Vijay Saraswat

December 1996 **ACM Computing Surveys (CSUR)**, Volume 28 Issue 4

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2 Implementing deductive databases by linear programming

Colin Bell, Anil Nerode, Raymond T. Ng, V. S. Subrahmanian

July 1992 **Proceedings of the eleventh ACM SIGACT-SIGMOD-SIGART symposium on Principles of database systems**

Full text available: pdf(926.92 KB) Additional Information: full citation, references, citings, index terms

3 Postpass Code Optimization of Pipeline Constraints

John L. Hennessy, Thomas Gross

July 1983 **ACM Transactions on Programming Languages and Systems (TOPLAS)**, Volume 5 Issue 3

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4 Scheduling techniques for embedded systems: An integer linear programming based approach for parallelizing applications in On-chip multiprocessors

I. Kadayif, M. Kandemir, U. Sezer

June 2002 **Proceedings of the 39th conference on Design automation**

Full text available: pdf(174.70 KB) Additional Information: full citation, abstract, references, index terms

With energy consumption becoming one of the first-class optimization parameters in computer system design, compilation techniques that consider performance and energy simultaneously are expected to play a central role. In particular, compiling a given application code under performance and energy constraints is becoming an important problem. In this paper, we focus on an on-chip multiprocessor architecture and present a parallelization strategy based on integer linear programming. Given an array ...

Keywords: constraint-based compilation, embedded systems, loop-Level parallelism

5 Embedded system synthesis by timing constraints solving

Krzysztof Kuchcinski

September 1997 **Proceedings of the 10th international symposium on System synthesis**

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This paper presents an approach to embedded system synthesis which minimizes a system cost while implementing given timing requirements. The embedded system is represented by a set of finite domain constraints defining different requirements on processes timing, system resources and interprocess communication. The assignment of processes to processors and interprocess communications to buses as well as their scheduling are then defined as an optimization problem. A prototype system, based on con ...

Keywords: Embedded Systems, Synthesis, Constraint Logic Programming.

6 Some challenges for constraint programming

Manuel Hermenegildo

December 1996 **ACM Computing Surveys (CSUR)**

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7 TDL: a hardware description language for retargetable postpass optimizations and analyses

Daniel Kästner

September 2003 **Proceedings of the second international conference on Generative programming and component engineering**

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The hardware description language TDL has been designed with the goal to generate machine-dependent postpass optimizers and analyzers from a concise specification of the target processor. TDL is assembly-oriented and provides a generic modeling of irregular hardware constraints that are typical for many embedded processors. The generic modeling supports graph-based and search-based optimization algorithms. An important design goal of TDL was to achieve extendibility, so that TDL can be easily i ...

8 Optimal FPGA module placement with temporal precedence constraints

S. Fekete, E. Köhler, J. Teich

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Frank M. Johannes

June 1996 **Proceedings of the 33rd annual conference on Design automation**

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6 **Generalized ILP scheduling and allocation for high-level DSP synthesis**
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Qiang Zhang; Liou, J.J.; McMacken, J.; Thomson, J.; Layman, P.;
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Pages:1885 - 1891

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Pages:359 - 372

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3 **Multimedia ASIP SoC codesign based on multicriteria optimization**
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